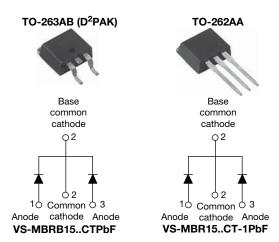


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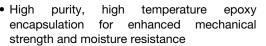
High Performance Schottky Rectifier, 2 x 7.5 A



PRODUCT SUMMARY					
Package	TO-263AB (D ² PAK), TO-262AA				
I _{F(AV)}	2 x 7.5 A				
V_{R}	35 V, 45 V				
V _F at I _F	0.57 V				
I _{RM} max.	15 mA at 125 °C				
T _J max.	150 °C				
Diode variation	Common cathode				
E _{AS}	7.0 mJ				

FEATURES

- 150 °C T_J operation
- Center tap TO-220 package
- · Low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-MBR(B)15... center tap Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	15	A		
V _{RRM}		35, 45	V		
I _{FSM}	t _p = 5 μs sine	690	A		
V _F	7.5 A _{pk} , T _J = 125 °C	0.57	V		
TJ		-65 to +150	°C		

VOLTAGE RATINGS					
PARAMETER SYMBOL VS-MBRB1535CTPbF VS-MBRB1545CTPbF VS-MBR1535CT-1PbF VS-MBR1545CT-1PbF UNITS					
Maximum DC reverse voltage	V_{R}	35	45	V	
Maximum working peak reverse voltage	V_{RWM}	33	45	V	

ABSOLUTE MAXIMUM RATI	NGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS		
Maximum average per leg			T 404 %0				
forward current per device	I _{F(AV)}	$I_C = 131$ G, rated	$T_C = 131$ °C, rated V_R		a v _R		
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	690	Α		
non-repetitive surge	I _{FSM}	Surge applied at rated load conditions halfwave, single phase, 60 Hz		150			
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{A}, L = 3.5 \text{mH}$		7	mJ		
Repetitive avalanche current per leg	I _{AR}	, ,	linearly to zero in 1 μ s by T _J maximum V _A = 1.5 x V _R typical	2	Α		

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS		
		15 A	T _J = 25 °C	0.84	
Maximum forward voltage drop	V _{FM} ⁽¹⁾	7.5 A	T 105 00	0.57	V
		15 A	T _J = 125 °C	0.72	
Maximum instantaneous reverse current	I _{RM} ⁽¹⁾	T _J = 25 °C	Rated DC voltage	0.1	- mA
Maximum instantaneous reverse current		T _J = 125 °C	hated DC voltage	15	
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal ran	ge 100 kHz to 1 MHz), 25 °C	400	pF
Typical series inductance	L _S	Measured from top of terr	minal to mounting plane	8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R	_	10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction temperature range		TJ		-65 to +150	°C	
Maximum storage tempera	ture range	T _{Stg}		-65 to +175	C	
Maximum thermal resistance, junction to case per leg		R _{thJC}	DC operation	3.0		
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	°C/W	
Maximum thermal resistance, junction to ambient		R _{thJA}	DC operation	60		
Approximate weight				2	g	
Approximate weight				0.07	oz.	
minimum				6 (5)	kgf · cm	
Mounting torque m	maximum			12 (10)	(lbf \cdot in)	
Marking device			Case style D ² PAK	MBRB1	545CT	
			Case style TO-262	MBR1545CT-1		



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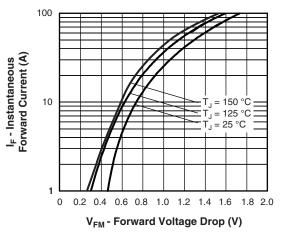


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

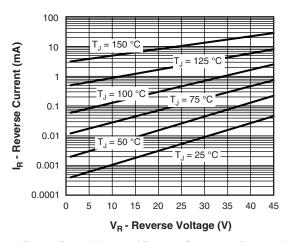


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

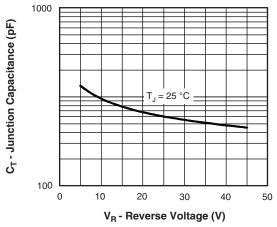


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

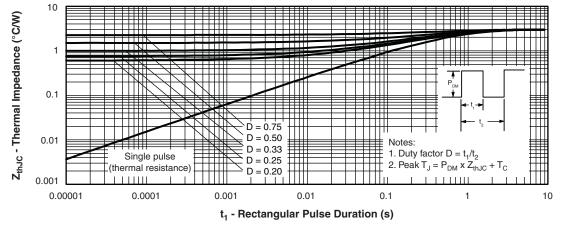


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



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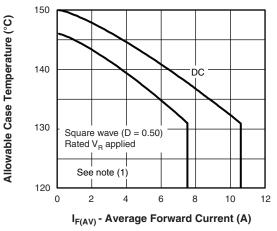


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

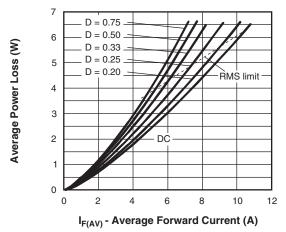


Fig. 6 - Forward Power Loss Characteristics

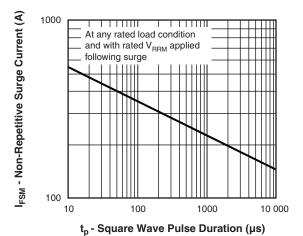


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

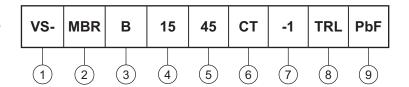
Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6);} \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$

Vishay Semiconductors

ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Essential part number
- 3 • B = D²PAK 7 None
 - None = TO-262 7 = -1
- Current rating (15 = 15 A)
- 5 Voltage ratings 45 = 45 V
- CT = essential part number
- 7 • None = D²PAK 3 = B • -1 = TO-262 3 None
- 8 • None = tube (50 pieces)
 - TRL = tape and reel (left oriented for D²PAK only)
 - TRR = tape and reel (right oriented for D²PAK only)
- 9 • PbF = lead (Pb)-free (for TO-262 and D²PAK tube)
 - P = lead (Pb)-free (for D²PAK TRR and TRL)

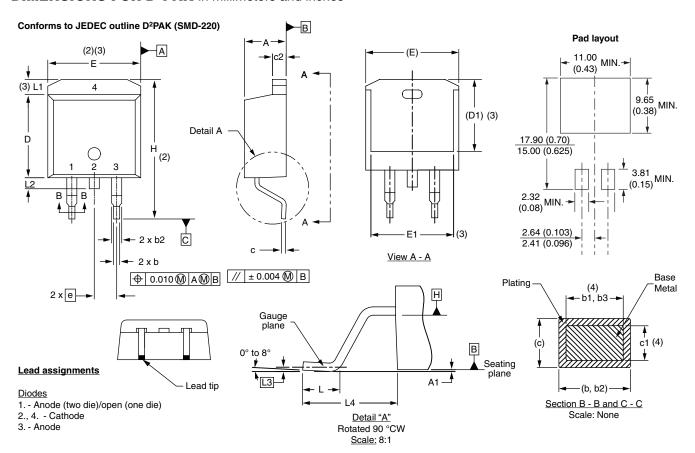
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95014			
Part marking information	www.vishay.com/doc?95008			
Packaging information	www.vishay.com/doc?95032			
SPICE model	www.vishay.com/doc?95294			



Vishay High Power Products

D²PAK, TO-262

DIMENSIONS FOR D²PAK in millimeters and inches



ovupo.	MILLIM	IETERS	INC		
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIM	ETERS	INCHES		NOTES	
	MIN.	MAX.	MIN.	MAX.	NOTES	
D1	6.86	8.00	0.270	0.315	3	
E	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54 BSC		0.100 BSC			
Н	14.61	15.88	0.575	0.625		
L	1.78	2.79	0.070	0.110		
L1	-	1.65	-	0.066	3	
L2	1.27	1.78	0.050	0.070		
L3	0.25 BSC		0.010	BSC		
L4	4.78	5.28	0.188	0.208		

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}\,$ Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch

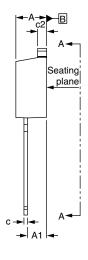
(7) Outline conforms to JEDEC outline TO-263AB

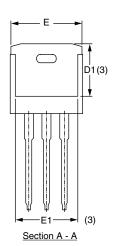
Vishay High Power Products

D²PAK, TO-262



DIMENSIONS FOR TO-262 in millimeters and inches





⊕ 0.010**⋒**|A**⋒**|B

Lead assignments

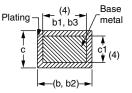


<u>Diodes</u>

-3 x b2 --3 x b

> 1. - Anode (two die)/open (one die) 2., 4. - Cathode

3. - Anode



Section B - B and C - C Scale: None

OVMBOL	MILLIMETERS		INC	INCHES		
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
E	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54 BSC		0.10	0 BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



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